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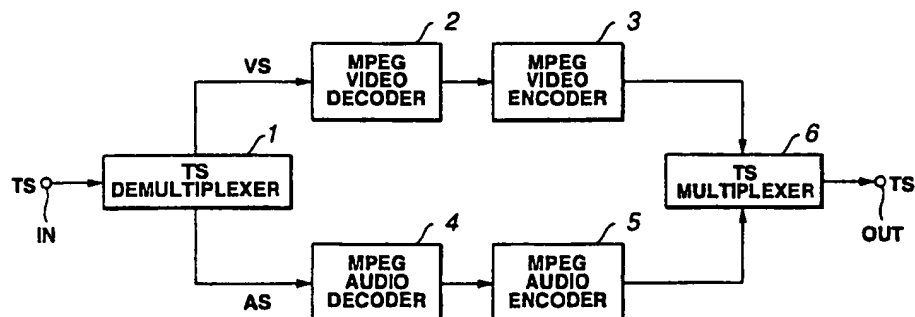
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(54) **METHOD AND APPARATUS FOR TRANSCODING, AND MEDIUM**

(57) In a video TS packet separator, a video TS packet VTSP is extracted from a TS (transport stream). In a PES packet analyzer through a TS packet generator, the video TS packet is decoded, re-encodes at a pre-determined bit rate, TS-packetized and supplied to a

switch input terminal. The switch input terminal is also supplied with non-video TS packets UVTSP from a TS packet buffer. A controller is provided to control the TS packet generator and switch based on timing information and PCR from a TS packet timing manager. Thus, codes in the TS can be converted.



**FIG.1**

**BEST AVAILABLE COPY**

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## Description

### Technical Field

[0001] The present invention relates to a transcoding apparatus, method and medium, and more particularly to a transcoding apparatus, method and medium, for converting codes used in a transport stream having multiplexed therein a video stream, audio stream and other data stream.

### Background Art

[0002] In the satellite digital television broadcasting and the ground-wave digital television broadcasting which is going to start soon, a transport stream (will be referred to simply as "TS" hereinafter) conforming to the MPEG (Moving Pictures coding Experts Group) 2 systems (ISO/IEC 13818-1) (will be referred to as "MPEG-2" hereinafter), is used as the broadcast wave. TS as the broadcast wave has multiplexed therein a video stream, audio stream and other data stream.

[0003] When TS as the digital television broadcast wave is recorded into a recording medium, a bit rate changer as shown in FIG. 1 is used to reduce the code bit rate for a to-be-recorded TS if the data recording rate into the recording medium is lower than at a bit rate at which the TS is to be recorded. Note that since the bit rate changer functions to convert the code bit rate, it is a kind of transcoder for converting codes of an input stream.

[0004] A TS demultiplexer 1 included in the bit rate changer shown in FIG. 1 separates an input TS via an input terminal IN into a video stream VS and audio stream AS and supplies them to an MPEG video decoder 2 and MPEG audio decoder 4, respectively.

[0005] The MPEG video decoder 2 decodes the supplied video stream VS to provide a video signal and supplies the video signal to an MPEG video encoder 3. The MPEG video encoder 3 will encode the supplied video signal to a video stream having a predetermined bit rate by the MPEG method, and supply the encoded video stream to a TS multiplexer 6. On the other hand, the MPEG audio decoder 4 decodes the supplied audio stream AS supplied to provide an audio signal and supplies the audio signal to an MPEG audio encoder 5. The MPEG audio encoder 5 will encode the supplied audio signal to an audio stream having a predetermined bit rate by the MPEG method, and supply the coded audio stream to the TS multiplexer 6. The TS multiplexer 6 will multiplex the video stream supplied from the MPEG video encoder 3 and the audio stream supplied from the MPEG audio encoder 5 to provide a TS' having a reduced bit rate. The TS' is delivered at an output terminal OUT.

[0006] However, the conventional bit rate changer shown in FIG. 1 is disadvantageous in that when an input TS contains any elementary streams not conforming to

MPEG-2, the TS, once separated into elementary streams, cannot be re-multiplexed. For example, a private stream such as a data stream used in the digital television broadcasting is not conforming to MPEG-2, and each of service providers uniquely sets necessary buffer size and bit rate for multiplexing. No multiplexing is possible when the TS multiplexer 6 does not understand the buffer model for a stream to be multiplexed. Therefore, it is difficult to multiplex a private stream such as data stream for example except when the buffer model of the stream is already known.

[0007] Also, the conventional bit rate changer shown in FIG. 1 has a problem that it can change the bit rate of a transport stream TS having multiplexed therein one video stream VS and one audio stream AS, each conforming to MPEG-2 but cannot change the bit rate of a TS having multiplexed therein one video stream VS and a plurality of audio streams AS.

### Disclosure of the Invention

[0008] Accordingly, the present invention has an object to overcome the above-mentioned drawbacks of the prior art by enabling to separate an input TS containing any elementary streams not conforming to MPEG-2 into elementary streams and re-multiplex the elementary streams.

[0009] The present invention has another object to permit to change the bit rate of a TS having multiplexed therein a plurality of elementary streams besides video streams.

[0010] The above object can be attained by providing a transcoder for generating, from a first multiplexed stream, a second multiplexed stream, including according to the present invention:

means for separating a first elementary stream from the first multiplexed stream supplied;  
means for converting the first elementary stream separated by the separating means by a predetermined method to a signal;  
means for packetizing the signal converted by the converting means to generate a first packet;  
means for storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and  
means for multiplexing, based on the timing information stored in the storing means, the first packet generated by the packetizing means and a second packet containing the second elementary stream to generate the second multiplexed stream.

[0011] In the above transcoder according to the present invention, the converting means includes:

means for decoding the first elementary stream separated by the separating means to generate an

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original signal corresponding to the first elementary stream; and  
means for encoding the original signal generated by the decoding means at a predetermined bit rate.

[0012] Also in the above transcoder, the converting means converts, by a predetermined method, codes forming the first elementary stream separated by the separating means.

[0013] Also in the above transcoder, the multiplexing means multiplexes, based on the timing information stored in the storing means, the second packet to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream.

[0014] Also the above object can be attained by providing a transcoding method for generating, from a first multiplexed stream, a second multiplexed stream, including, according to the present invention, steps of:

separating a first elementary stream from the first multiplexed stream supplied;  
converting the first elementary stream separated at the separating step by a predetermined method to a signal;  
packetizing the signal converted at the converting step to generate a first packet;  
storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and  
multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.

[0015] In the above transcoding method according to the present invention, the converting step includes steps of:

decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and  
encoding the original signal generated at the decoding step at a predetermined bit rate.

[0016] Also in the above transcoding method, at the converting step, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.

[0017] Also in the above transcoding method, at the multiplexing step, the second packet is multiplexed to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream based on the timing information stored at the storing step.

[0018] Also the above object can be attained by pro-

viding a medium having recorded therein a transcoding program to generate, from a first multiplexed stream, a second multiplexed stream and which is to be executed by a computer, the program including, according to the present invention, steps of:

separating a first elementary stream from the first multiplexed stream supplied;  
converting the first elementary stream separated at the separating step by a predetermined method to a signal;  
packetizing the signal converted at the converting step to generate a first packet;  
storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and  
multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.

[0019] In the above medium according to the present invention, the converting step in the program includes steps of:

decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and  
encoding the original signal generated at the decoding step at a predetermined bit rate.

[0020] Also in the above medium, at the converting step in the program, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.

[0021] Also in the above medium, at the multiplexing step in the program, the second packet is multiplexed to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream based on the timing information stored at the storing step.

[0022] These objects and other objects, features and advantages of the present invention will become more apparent from the following detailed description of the preferred embodiments of the present invention when taken in conjunction with the accompanying drawings.

#### Brief Description of the Drawings

[0023] FIG. 1 is a block diagram of a conventional bit rate changer, showing an example of its construction.

[0024] FIG. 2 is a block diagram of an embodiment of the transcoder according to the present invention.

[0025] FIG. 3 is a block diagram of a code converter included in the transcoder in FIG. 2.

[0026] FIG. 4 is a block diagram of a TS recorder us-

ing the transcoder in FIG. 2, showing an example of its construction.

[0027] FIG. 5 explains the operation of the TS recorder in FIG. 4.

[0028] FIG. 6 is a flow chart of operations effected in switching control by a controller included in the transcoder in FIG. 2.

[0029] FIG. 7 is a block diagram of another embodiment of the transcoder according to the present invention.

[0030] FIG. 8A shows a personal computer incorporating a recording medium having stored therein a transcoding program.

[0031] FIG. 8B explains the recording medium used to install the transcoding program into the personal computer and enable the execution of the program.

[0032] FIG. 8C explains a system used to install the transcoding program into the personal computer and enable the execution of the program.

[0033] FIG. 9 is a block diagram of the personal computer, showing its internal construction.

#### Best Mode for Carrying Out the Invention

[0034] Referring now to FIG. 2, there is illustrated in the form of a block diagram an embodiment of the transcoder according to the present invention. This transcoder is to generate, from a first transport stream TS supplied as an input, a second transport stream TS'. The first transport stream TS is a one having multiplexed therein a video signal transport stream packet as a first elementary stream (will be referred to as "video TS packet VTSP" hereinafter) and non-video TS packets UVTSP including audio stream and other data stream.

[0035] As shown in FIG. 2, the transcoder includes a video TS packet separator 10 to separate the video TS packet VTSP from the first transport stream TS supplied from an input terminal IN, a code converter 12 to convert, by a predetermined method, codes forming the video TS packet VTSP separated by the video TS packet separator 10, a TS packet generator 15 to generate a code-converted video TS packet VTSP' by packetizing the signal whose codes have been converted by the code converter 12, a TS timing manager 18 to store timing information indicating a time at which the packet including the non-video TS packets UVTSP forming the first transport stream TS appears in the first transport stream TS, and a switch 16 to generate, and deliver at an output terminal OUT, the second transport stream TS' by multiplexing the video TS packet VTSP' generated by the TS packet generator 15 and non-video TS packet UVTSP based on the timing information stored in the TS packet timing manager 18. The switch 16 functions as a multiplexer under the control of a controller 19 which will further be described later.

[0036] The transcoder further includes a PES packet analyzer 11 provided between the video TS packet separator 10 and code converter 12, the controller 19 pro-

vided between the TS packet timing manager 18 and switch 16, and a TS packet buffer 17.

[0037] The video TS packet separator 10 extracts a video TS packet VTSP from the first transport stream TS supplied from the input terminal IN, and provides it to the PES packet analyzer 11. Also, the video TS packet separator 10 extracts, from the first transport stream TS, the non-video TS packet UVTSP and provides it to the TS packet buffer 17. Further, the video TS packet separator 10 supplies the TS packet timing manager 18 with timing information indicating a time at which the non-video TS packet UVTSP has been supplied as an input (information indicating a time at which the non-video TS packet appears in the first transport stream TS).

[0038] The PES packet analyzer 11 uses the pay load of the video TS packet VTSP supplied from the video TS packet separator 10 to form a PES (packetized elementary stream) and provides the code converter 12 with a encoded video signal being the pay load of the PES packet (will be referred to as "encoded video signal" hereinafter). Also the PES packet analyzer 11 provides the code converter 12 with a PTS (presentation time stamp) of the encoded video signal. Note here that the PTS is a parameter described in the header of the PES packet and indicates a time on the time base when the encoded video signal is to be reproduced.

[0039] The code converter 12 converts, by the predetermined method, the encoded video signal generated by the PES packet analyzer 11 from the video TS packet VTSP separated by the video TS packet separator 10. For example, when the transcoder is used as a bit rate converter shown in FIG. 1, the code converter 12 decodes the encoded video signal, then re-encodes it at a predetermined bit rate (compression rate) and supplies it to the TS packet generator 15. Also, the transcoder may convert codes concerning copyright protection information, for example, appended to the encoded video signal.

[0040] First, bit rate conversion will be described below. In this case, the code converter 12 includes an MPEG video decoder 13 and MPEG video encoder 14 as shown in FIG. 3. The MPEG video decoder 13 decodes the encoded video signal supplied from the PES packet analyzer 11, and provides the decoded video signal to the MPEG video encoder 14. The MPEG video decoder 13 will also provide the MPEG video encoder 14 with a PTS (presentation time stamp) corresponding to the video signal. The MPEG video encoder 14 re-encodes, at a predetermined bit rate (compression rate), the video signal supplied from the MPEG video decoder 13, and provides the TS packet generator 15 with the re-encoded video signal along with a PTS corresponding to the re-encoded video signal. Under the control of the controller 19, the TS packet generator 15 packetizes the encoded video signal supplied from the MPEG video encoder 14 to generate a video TS packet VTSP', and delivers it at an input terminal a of the switch 16.

[0041] The switch 16 is controlled by the controller 19

to turn the output thereof to either the input terminal a or b thereof. The switch 16 will provide, via the output terminal OUT, either the video TS packet VTSP' supplied to the input terminal a thereof from the TS packet generator 15 or the second packet stream TS' generated by time-division multiplexing the non-video TS packets UVTSP, supplied at the input terminal b thereof from the TS packet buffer 17.

[0042] The TS packet buffer 17 stores non-video TS packets UVTSP supplied from the video TS packet separator 10 and delivers them one after another at the input terminal b of the switch 16.

[0043] The TS packet timing manager 18 manages timing information supplied from the video TS packet separator 10 and corresponding to the non-video TS packet UVTSP, stored in the TS packet buffer 17 (information indicating a time at which the TS packet UVTSP appears in the TS) and a PCR (program clock reference) indicating a time on the time base of the TS, and supplies them to the controller 19.

[0044] The controller 19 controls the TS packet generator 15 and switch 16 based on the timing information and PCR supplied from the TS packet timing manager 18.

[0045] The transcoder constructed to convert a bit rate as in the above is used in a transport stream (TS) recorder shown in FIG. 4. The TS recorder records into a recording medium which will further be described later the second transport stream TS' generated by multiplexing the video TS packet VTSP' whose bit rate has been reduced by the code converter 12 of the transcoder, and non-video TS packet UVTSP.

[0046] The TS recorded shown in FIG. 4 includes a transcoder as shown in FIG. 1 and a recording system including a bit stream purser 20 to recording medium 28.

[0047] The bit stream purser 20 reads a PCR from TS packets supplied one after another from the switch 16 and provides them to a PLL (phase locked loop) block 21 and other to an Arrival\_time stamp append block 23. The PLL block 21 locks a 27-MHz clock of the recording system to the PCR supplied from the bit stream purser 20. The recording system also includes a time stamp generator 22 which generates a time stamp based on a count of 27-MHz clocks.

[0048] When providing a TS packet supplied from the bit stream purser 20 to a smoothing buffer 24 also included in the recording system, the Arrival\_time stamp append block 23 appends to the TS packet a time stamp indicating a time at which a first byte of the TS packet is to be provided to the smoothing buffer 24. The smoothing buffer 24 stores the TS packet having the time stamp appended thereto, and provides it in units of a predetermined data amount appropriately to an error correction block 25 also included in the recording system. The error correction block 25 appends an error correction code to the TS packet supplied from the smoothing buffer 24 and provides it to a modulator 26 also included in the recording system. The modulator 26 modulates the TS

packet supplied from the error correction block 25 by a predetermined method, and provides it to a recorder 27 also included in the recording system. The recorder 27 records the modulated TS packet supplied from the modulator 27 to the recording medium 28.

[0049] The transcoder used in the TS recorder functions as will be described below with reference to FIG. 5. Note that actually, TS packets shown in FIG. 5D are delayed relative to TS packets shown in FIGS. 5A to 5C for time lengths of the operations of the PES packet analyzer 11 to TS packet generator 15. The non-video TS packets UVTSP are buffered in the TS packet buffer 17 and provided at an appropriate time (as shown in FIG. 5E) to synchronize these TS packets at each time indicated with a broken line in FIG. 5.

[0050] When a TS having time-division multiplexed therein video TS packets VTSP (Indicated with "V" in FIG. 5) shown in FIG. 5A and non-video TS packets UVTSP, is supplied from the video TS packet separator 10 to the video TS packet separator 10, the latter will extract the video TS packets VTSP from the TS as shown in FIG. 5B, and provide them to the PES packet analyzer 11. The video TS packet separator 10 will also extract the non-video TS packets UVTSP from the TS as shown in FIG. 5C, and provide them to the TS packet buffer 17. At the same time, the video TS packet separator 10 will provide the TS packet timing manager 18 with information of a time at which the non-video TS packet UVTSP has been supplied.

[0051] The PES packet analyzer 11 uses the pay load of the video TS packet VTSP supplied from the video TS packet separator 10 to provide a PES packet, and provides an encoded video signal being the pay load to the MPEG video decoder 13 in the code converter 12. The MPEG video decoder 13 decodes the encoded video signal supplied from the PES packet analyzer 11, and provides the decoded video signal to the MPEG video encoder 14.

[0052] The MPEG video encoder 14 re-encodes the video signal supplied from the MPEG video decoder 13 at a predetermined bit rate (half of the bit rate of the TS packets shown in FIG. 5B when the video signal is the TS packets shown in FIG. 5D), and provides it, along with PTS to which the encoded video signal corresponds, to the TS packet generator 15. Under the control of the controller 19, the TS packet generator 15 packetizes the encoded video signal from the MPEG video encoder 14 as shown in FIG. 5D, and provides it to the input terminal a of the switch 16.

[0053] On the other hand, the TS packet buffer 17 supplies the non-video TS packets to the input terminal b of the switch 16 with a delay of a predetermined time as shown in FIG. 5E.

[0054] The controller 19 controls the switch 16 to turn the output thereof to its input terminal a or b (as will further be described later with reference to the flow chart of FIG. 6).

[0055] Thus, the video TS packets VTSP' and non-

video TS packets UVTSP are time-division multiplexed as shown in FIG. 5F are provided from the switch 16 to the bit stream purser 20 located downstream of the switch 16.

[0056] The TS packets not continuous to each other as shown in FIG. 5F will be rearranged to be close to each other by the downstream smoothing buffer 24 in the recording system shown in FIG. 4 and then recorded in the recording medium 28.

[0057] Next, the switching by the controller 19 will be described herebelow with reference to the flow chart in FIG. 6. First at step S1, the controller 19 judges, based on the timing information and PCR supplied from the TS packet timing manager 18, whether the present time (elapsed from a time when the video TS packet separator 10 was supplied with TS) is a time to provide non-video TS packets UVTSP on the time base of the TS. If it determines that now is not any time to provide the non-video TS packet UVTSP, it goes to step S2.

[0058] At step S2, the controller 19 judges whether the TS packet generator 15 has been supplied with an encoded video signal to be TS-packetized from the MPEG encoder 14. If it determines that the encoded video signal to be TS-packetized is not supplied, it goes to step S3.

[0059] At step S3, the TS packet generator 15 will be controlled by the controller 19 to packetize the encoded video signal from the MPEG video encoder 14 and provide it to the input terminal a of the switch 16. The switch 16 will be controlled by the controller 19 to turn an output thereof to the input terminal a and provide the video TS packet VTSP supplied from the TS packet generator 15 to the downstream bit stream purser 20 shown in FIG. 4.

[0060] At step S5, the controller 19 judges whether all TS packets have been provided to the stage downstream of the switch 16. If it determines that all the TS packets have not yet been provided, it goes back to step S1.

[0061] If it is determined at step S1 that the present time is a time to provide the non-video TS packet UVTSP, the controller 19 will go to step S4. At step S4, the switch 16 is controlled by the controller 19 to turn the output thereof to the input terminal b and provide the non-video TS packet UVTSP supplied from the TS packet buffer 17 to the bit stream purser 20 located downstream of the switch 16.

[0062] Thereafter, the controller 19 will go back to step S1 until it determines at step S5 that all the TS packets have been provided, and the operations at steps S1 and subsequent steps are repeated. When the controller 19 determines that all the TS packets have been provided, the switching control will be ended.

[0063] As in the above, the transcoder functioning to convert a bit rate can convert only the bit rate of video streams contained in a TS to generate a new TS whether or not non-video elementary streams, contained in the TS are conforming to MPEG-2.

[0064] Also the transcoder can convert the bit rate of

video streams contained in a TS irrespectively of the number of non-video elementary streams contained in the TS to generate a new TS.

[0065] The TS recorder can convert only the bit rate of video streams contained in a TS to generate and record a new TS whether or not non-video elementary streams contained in the TS are conforming to MPEG-2.

[0066] Also the TS recorder can convert the bit rate of video streams contained in a TS irrespectively of the number of non-video elementary streams to generate and record a new TS.

[0067] Note that the present invention is applicable to decoding of a video bit stream forming an input multiplexed stream to generate an original signal, and re-encoding of the original signal to convert the bit rate of the video bit stream forming the input multiplexed stream as well as to converting of codes appended to the video bit stream by a predetermined method, re-appending the converted code to the video bit stream and providing them.

[0068] For example, the present invention is applicable to conversion by a predetermined method of a code concerning a copyright protection information appended to a video bit stream forming an input multiplexed stream as shown in FIG. 7. The transcoder in FIG. 7 is different from that shown in FIG. 2 in that a copyright protection information converter 30 is used instead of the code converter 12 in FIG. 12, the output of the PES packet analyzer 11 is analyzed by an analyzer 31 and code conversion in the copyright protection information converter 30 is controlled according to the result of analysis from the analyzer 31.

[0069] The analyzer 31 analyzes the output of the PES packet analyzer 11 to detect copyright protection information such as copy control information (CCI), water mark signal, duplication generation information limiting the number of duplications, etc. embedded in an input video signal. The copyright protection information converter 30 updates, based on the result of analysis from the analyzer 31, codes concerning the copyright protection information by a predetermined method and provides an updated video stream.

[0070] For example, when the analyzer 31 detects duplication generation information, the copyright protection information converter 30 will count up the duplication generation information by one, if the generation limitation is within an allowable range, append the count to the video transport packet VTSP correspondingly to the PTS, and send the packet to the TS packet generator 15.

[0071] On the other hand, when the generation limitation is beyond the allowable range, the copyright protection information converter 30 will not provide the video TS packet VTSP to the TS packet generator 15.

[0072] Thus, the transcoder shown in FIG. 7 can provide a management to protect the copyright of the video transport stream.

[0073] The transcoder shown in FIG. 7 may be com-

combined with the recording system including the bit stream purser 20 thru the recording medium 28 shown in FIG. 4 to construct a TS recorder which will record TS including a video stream whose copyright is protected. Note that the TS recorder cannot record any TS depending upon the type of the copyright protection information, for example, when the generation limitation is beyond the allowable range.

**[0074]** Note that the present invention is applicable to conversion of the bit rate of a video stream included in a program stream or to conversion of a code appended to a video bit stream by a predetermined method.

**[0075]** Also the present invention is applicable to conversion of the bit rate of an audio stream in a TS or conversion of an audio stream in a TS by a predetermined method, and then multiplexing the audio stream with other elementary stream to generate a new TS.

**[0076]** Further the present invention is applicable to conversion of the bit rate of a plurality of elementary streams such as both a video stream and audio stream or conversion of a plurality of elementary streams by a predetermined method, and then multiplexing them with other elementary stream to generate a new TS.

**[0077]** The aforementioned series of processes can be executed by a hardware and also by a software. For execution of the series of processes by a software, a program forming the software is installed in a computer built in a TS recorded as a dedicated hardware, or a general-purpose personal computer capable of performing a variety of functions with a variety of programs corresponding to the programs installed therein.

**[0078]** The medium according to the present invention having recorded therein a program intended for execution of the above series of processes and which is to be installed in a general-purpose personal computer which can execute the program, will be described herebelow with reference to FIGS. 8A to 8C.

**[0079]** The program can be provided to the user as being previously installed in a hard disc 302 or semiconductor memory 303, as a recording medium, incorporated in a personal computer 301, as shown in FIG. 8A.

**[0080]** Alternatively, the program can be provided to the user as a package software as being provisionally or permanently stored in a recording medium such as a floppy disc 311, CD-ROM (compact disc-read-only memory) 312, MO (magneto-optical) disc 313, DVD (digital versatile disc) 314, magnetic disc 315 or semiconductor memory 316, as shown in FIG. 8B.

**[0081]** Further, as shown in FIG. 8C, the program can be transferred by radio to the personal computer 301 from a download site 321 via a satellite 322 or by cable or radio to the personal computer 301 from the download site 321 via a network 331 such as local area network (LAN) or Internet, and stored in the hard disc 302 or the like incorporated in the personal computer 301.

**[0082]** The term "medium" referred to herein means a broad concept including all these media.

**[0083]** The personal computer 301 incorporates a

CPU (central processing unit) 342 as shown in FIG. 9 for example. The CPU 342 has an input/output interface 345 connected thereto via a bus 341. When supplied with a user's command via the input/output interface 345 from an input unit 347 including a keyboard, mouse, etc., the CPU 342 responds to the command to load into a RAM (random access memory) 344, and execute, a program stored in an ROM (read-only memory) 343 corresponding to the semiconductor memory 303 shown in FIG. 8A, a program transferred from the satellite 322 or network 331, received by the communication unit 348 and installed in the hard disc 302, or a program read out from the floppy disc 311, CD-ROM 312, MO disc 313, DVD 314 or magnetic disc 315 loaded in a drive 349 and installed in the hard disc 302. Further, the CPU 342 provides a result of a process executed thereby to a display unit 346 made of a LCD (liquid crystal display) or the like as necessary via the input/output interface 345 for example.

**[0084]** Note that in this specification, steps of describing the program provided to the user as stored in the medium include processes effected in a time series in the described order as well as processes effected in parallel or individually.

**[0085]** Note that the term "system" referred to herein means an entire assembly of a plurality of units.

#### Industrial Applicability

**[0086]** As having been described in the foregoing, according to the present invention, a first elementary stream is code-converted to generate a first packet. Also, timing information indicating a time at which a second packet forming a second elementary stream appears in a multiplexed stream is stored. The generated first packet and second packet are multiplexed based on the timing information, so the code of only the first elementary stream can be converted to generate a new second multiplexed stream whether or not the second elementary stream is conforming to MPEG-2. Also, it is possible to convert the codes of a TS having multiplexed therein a plurality of elementary streams in addition to video streams.

#### Claims

1. A transcoder for generating, from a first multiplexed stream, a second multiplexed stream, comprising:

means for separating a first elementary stream from the first multiplexed stream supplied;  
means for converting the first elementary stream separated by the separating means by a predetermined method to a signal;  
means for packetizing the signal converted by the converting means to generate a first packet;  
means for storing timing information indicating

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- a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and means for multiplexing, based on the timing information stored in the storing means, the first packet generated by the packetizing means and a second packet containing the second elementary stream to generate the second multiplexed stream.
2. The apparatus as set forth in Claim 1, wherein the converting means includes means for decoding the first elementary stream separated by the separating means to generate an original signal corresponding to the first elementary stream, and means for encoding the original signal generated by the decoding means at a predetermined bit rate.
  3. The apparatus as set forth in Claim 1, wherein the converting means converts, by a predetermined method, codes forming the first elementary stream separated by the separating means.
  4. The apparatus as set forth in Claim 1, wherein the multiplexing means multiplexes, based on the timing information stored in the storing means, the second packet to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream.
  5. The apparatus as set forth in Claim 1, wherein the first elementary stream is a video stream.
  6. A transcoding method for generating, from a first multiplexed stream, a second multiplexed stream, comprising the steps of:
    - separating a first elementary stream from the first multiplexed stream supplied;
    - converting the first elementary stream separated at the separating step by a predetermined method to a signal;
    - packetizing the signal converted at the converting step to generate a first packet;
    - storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and
    - multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.
  7. The method as set forth in Claim 6, wherein the converting step comprises the steps of:
    - decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and
    - encoding the original signal generated at the decoding step at a predetermined bit rate.
  8. The method as set forth in Claim 6, wherein at the converting step, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.
  9. The method as set forth in Claim 6, wherein at the multiplexing step, the second packet is multiplexed, based on the timing information stored at the storing step, to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream.
  10. The method as set forth in Claim 6, wherein the first elementary stream is a video stream.
  11. A medium having recorded therein a transcoding program to generate, from a first multiplexed stream, a second multiplexed stream and which is to be executed by a computer, the program comprising the steps of:
    - separating a first elementary stream from the first multiplexed stream supplied;
    - converting the first elementary stream separated at the separating step by a predetermined method to a signal;
    - packetizing the signal converted at the converting step to generate a first packet;
    - storing timing information indicating a time at which a packet, containing a second elementary stream forming the first multiplexed stream, appears in the multiplexed stream; and
    - multiplexing, based on the timing information stored at the storing step, the first packet generated at the packetizing step and a second packet containing the second elementary stream to generate the second multiplexed stream.
  12. The medium as set forth in Claim 11, wherein the converting step in the program comprises the steps of:
    - decoding the first elementary stream separated at the separating step to generate an original signal corresponding to the first elementary stream; and
    - encoding the original signal generated at the decoding step at a predetermined bit rate.
  13. The medium as set forth in Claim 11, wherein at the



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converting step in the program, codes forming the first elementary stream separated at the separating step are converted by a predetermined method.

14. The medium as set forth in Claim 11, wherein at the multiplexing step in the program, the second packet is multiplexed to the second multiplexed stream at a time corresponding to the time at which the second packet appears in the first multiplexed stream based on the timing information stored at the storing step.

15. The medium as set forth in Claim 11, wherein the first elementary stream is a video stream.

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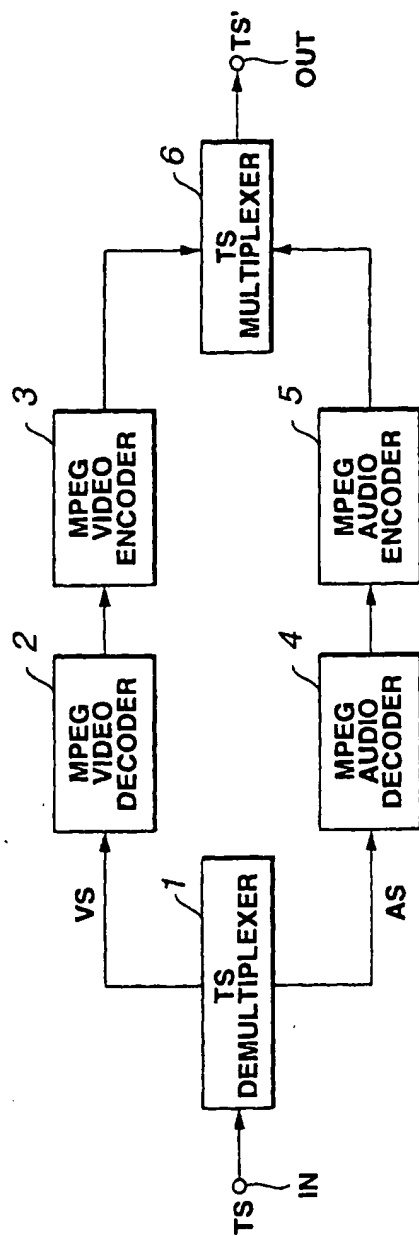


FIG.1

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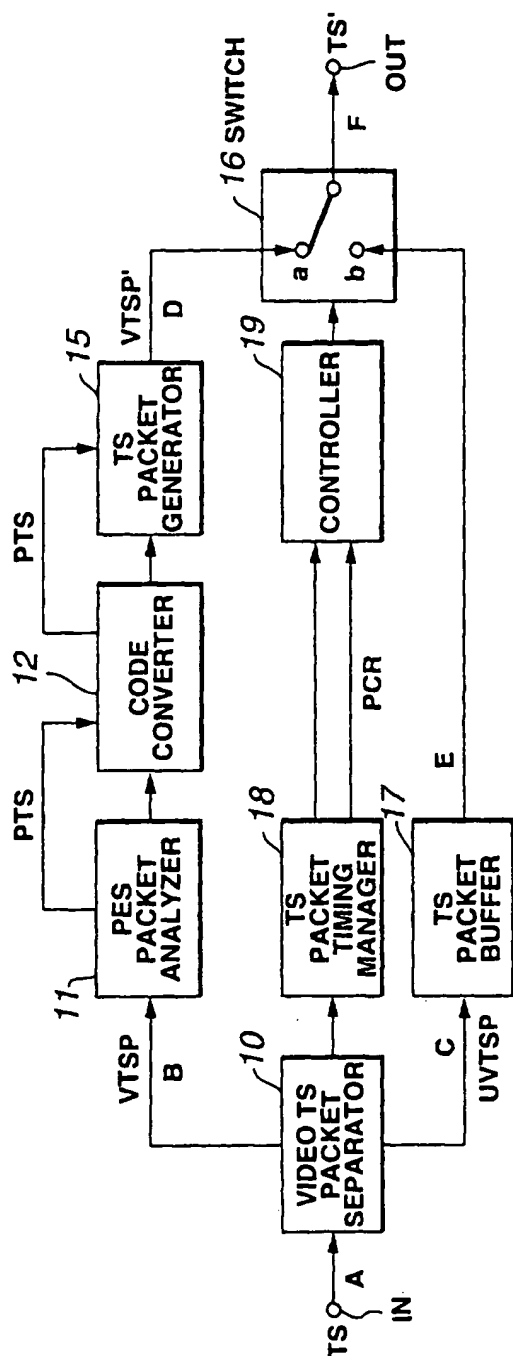


FIG.2

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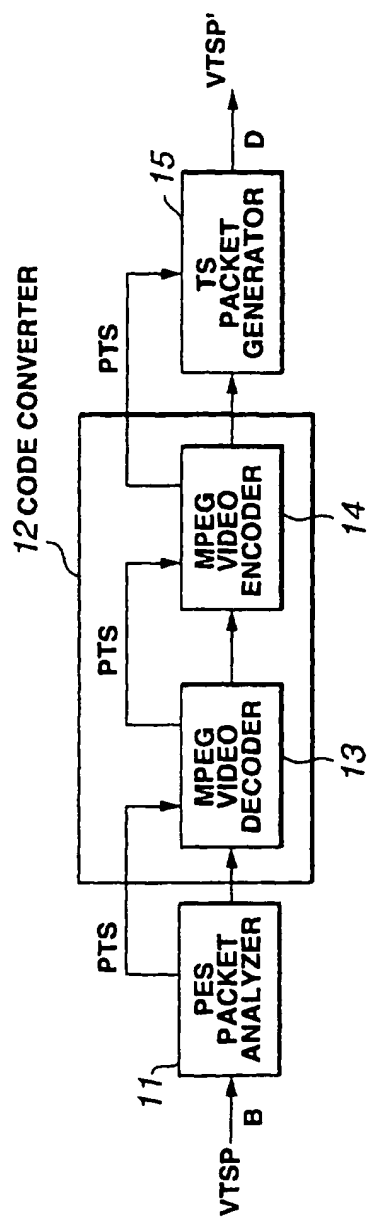


FIG.3



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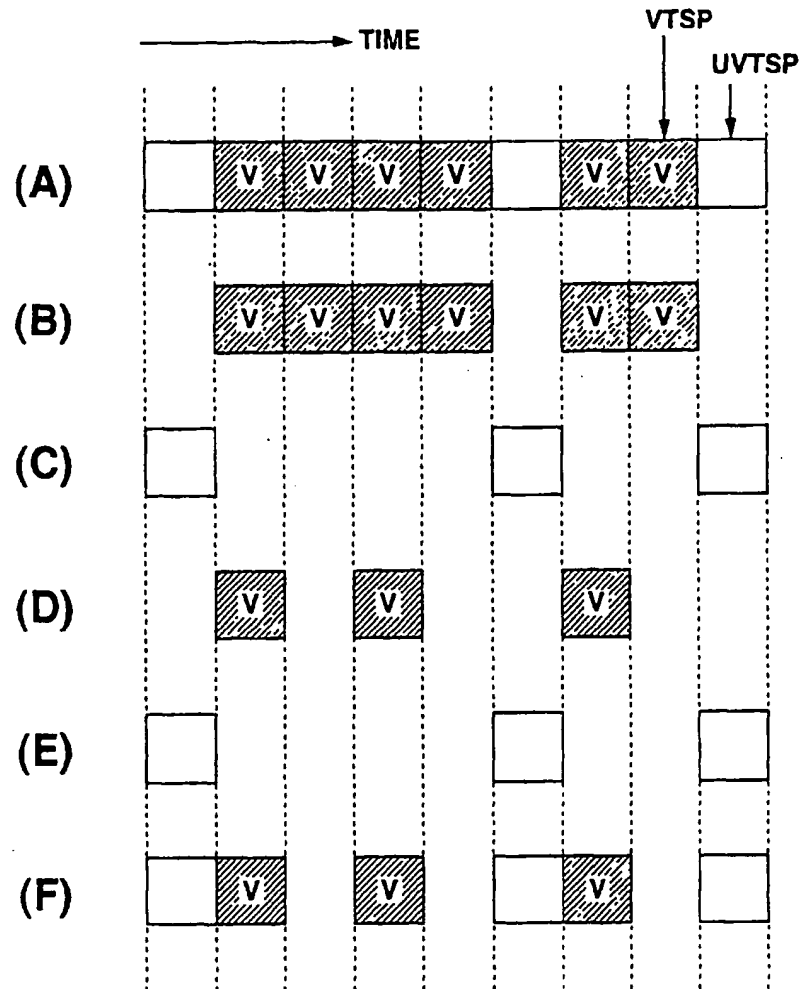


FIG.5

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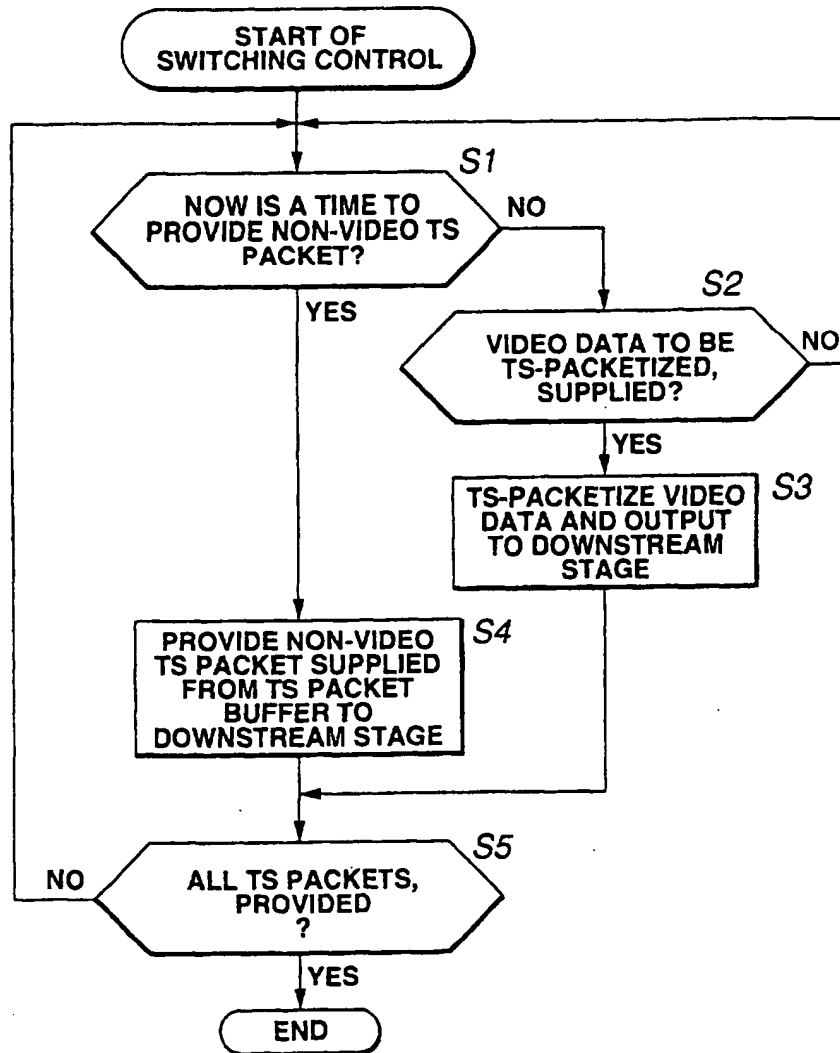


FIG.6

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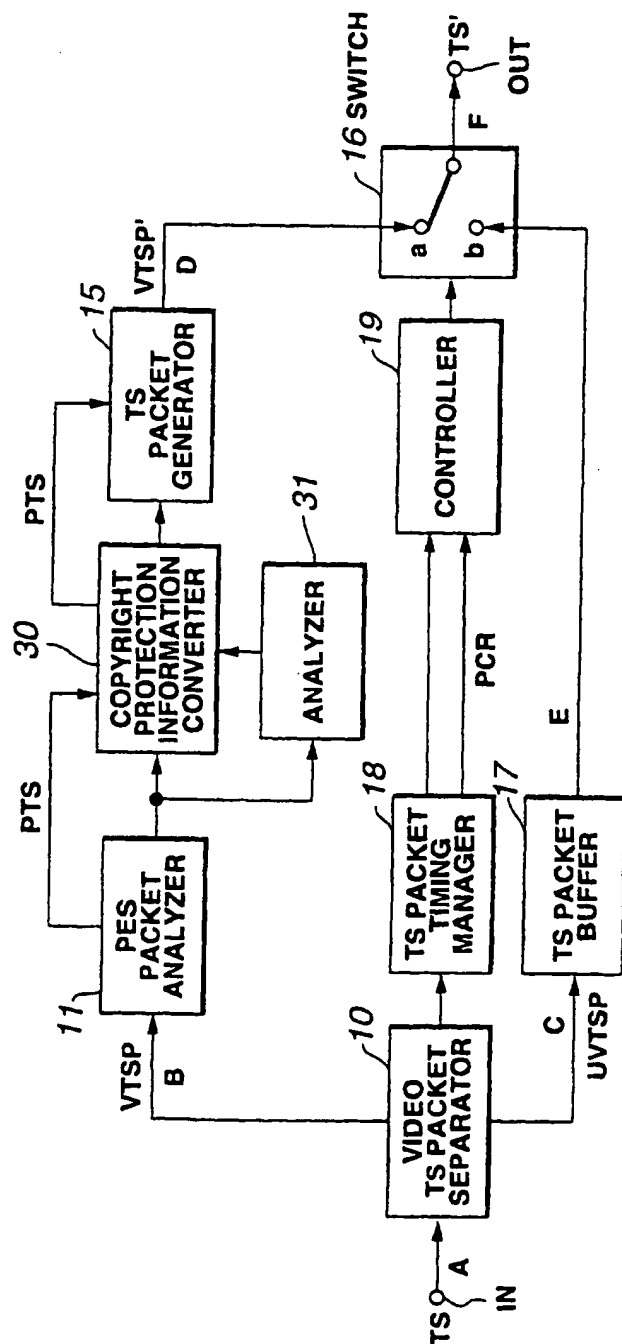


FIG. 7



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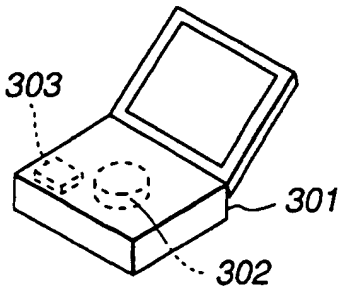


FIG. 8A

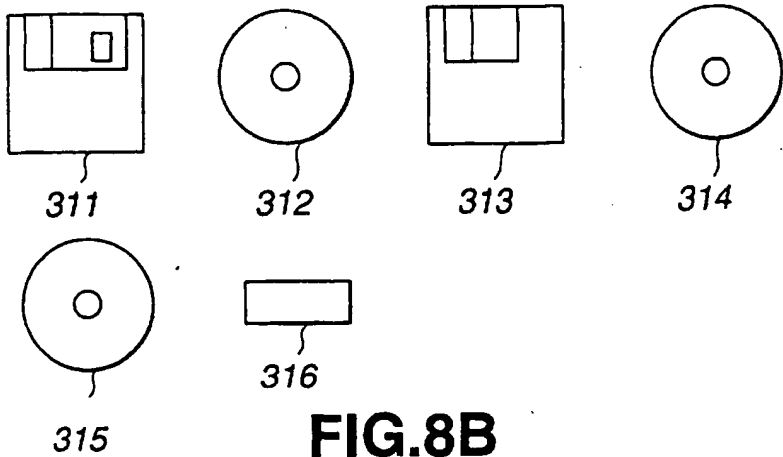


FIG. 8B

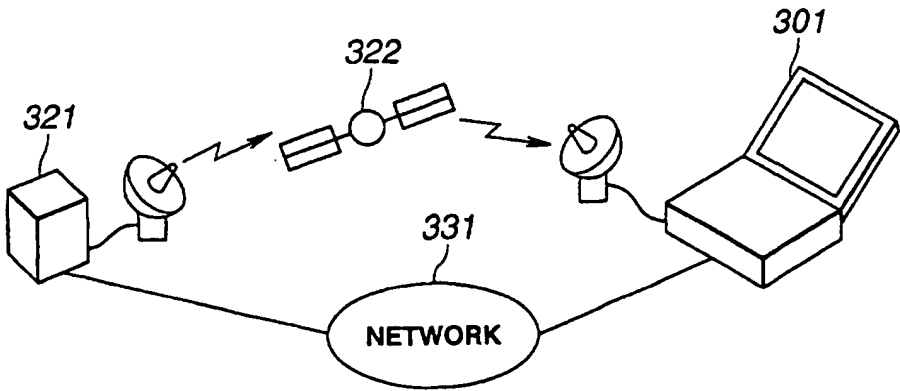


FIG. 8C

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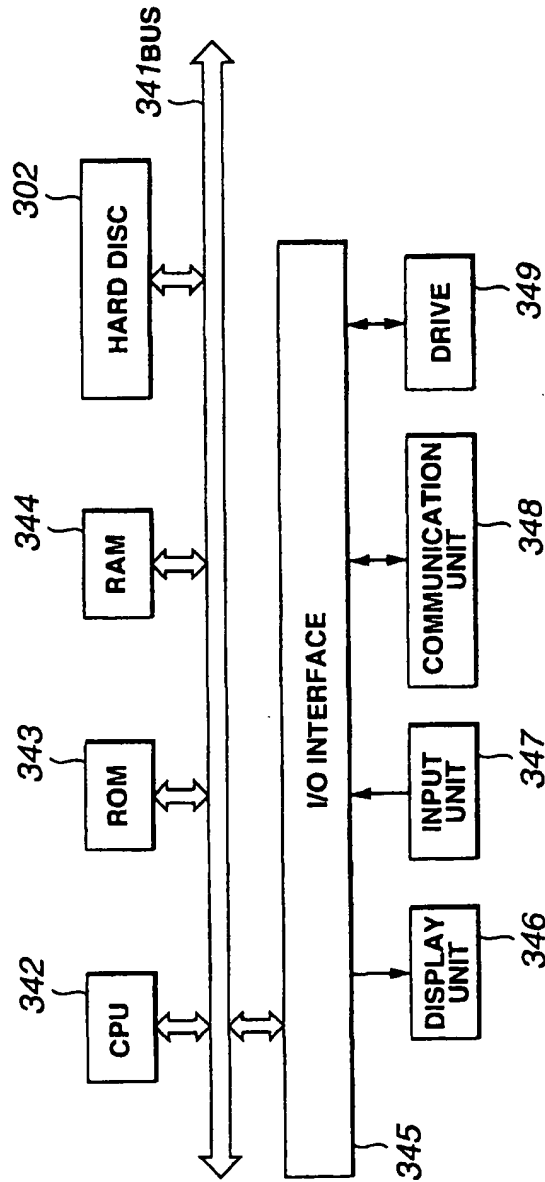


FIG.9

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## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP00/04375

<b>A. CLASSIFICATION OF SUBJECT MATTER</b> Int. Cl. <sup>7</sup> H03M 7/00 H04N 7/24 H04L13/00 H04L12/56 According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) Int. Cl. <sup>7</sup> H03M 7/00 H04N 7/24 H04L13/00 H04L12/56 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho (Y1, Y2) 1926-1996 Toroku Jitsuyo Shinan Koho (U) 1994-2000 Kokai Jitsuyo Shinan Koho (U) 1971-2000 Jitsuyo Shinan Toroku Koho (Y2) 1996-2000 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	DE, 19811292, A (LG Electronics Inc.), 24 September, 1998 (24.09.98), FIG. 3 & JP, 11-112454, A	1-15
A	JP, 8-23539, A (NIPPON HOSO KYOKAI), 23 January, 1996 (23.01.96), Fig. 1 (Family: none)	1-15
A	JP, 10-51769, A (Sony Corporation), 20 February, 1998 (20.02.98), Fig. 1 (Family: none)	1-15
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
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Date of the actual completion of the international search 26 September, 2000 (26.09.00)		Date of mailing of the international search report 03 October, 2000 (03.10.00)
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.

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